

TX Report

P 1

10/05/2006 08:51

Serial No. 31103384

TC: 88533

Destination	Start Time	Time	Prints	Result	Note
Hatori & Co.	10-05 09:51	00:00 18	001/001	OK	L1

Note L1: Main Circuit, L2: Sub Circuit, TMR: Timer, POL: Poll, ORG: Original,
FME: Frame Erase TX, MIX: Mixed Original, CALL: Manual Communication,
CSRC: CSRC, FWD: Forward, PC: PC-FAX, BND: Bind, SP: Special Original,
FCODE: F-Code, RTX: Re-Tx, RLY: Relay, MBX: Secure, BUL: Bulletin

Result OK: TX OK, S-OK: Stop Communication, PW-OFF: Power Switch OFF, TEL: RX from TEL,
NG: Other Error, Cont: Continue, No Ans: No Answer, Refuse: Receipt Refused,
Busy: Busy, M-Full: Memory Full.

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YOUNG & THOMPSON
International Patent Law
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September 26, 2006

Re: Your Ref: 8007-1060
Our Ref: AA04-2-US/OH
Masaki SHIMIZU et al. - U.S. Patent Appln. No. 10/812,179

Dear Colleagues:

With regard to the applicant's duty of disclosure, please be informed that the present application has a counterpart Chinese application and the first Office Action has recently been issued therein by the State Intellectual Property Office of China.

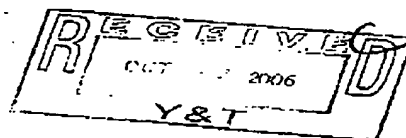
We enclose herewith copies of the above-mentioned "first Office Action" having a mailing date of July 21, 2006 and the English translation of the Office Action.

With reference to the cited reference, USP 4,743,531, it was cited in the European Search Report issued in the counterpart EP application, and was already filed in the Information Disclosure Statement dated September 10, 2004. Therefore, we do not send its copy.

Based on the aforementioned, please prepare and file a suitable Information Disclosure Statement by the due date of October 21, 2006.

Please acknowledge receipt of this letter by return facsimile.

Very truly yours,



Yoshiyuki Matsushima
HATORI & CO.

YM/ms
*Encls.